

# **OVERLAY MARKS, METHODS OF OVERLAY MARK DESIGN AND METHODS OF OVERLAY MEASUREMENTS**

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## **Cross Reference to Related Application**

This application claims the priority of U.S. Provisional No. 60/229,256 (Attorney Docket No. KLA1P018P), filed on August 30<sup>th</sup>, 2000, which is hereby incorporated by reference.

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## **Background of the Invention**

The present invention relates generally to overlay measurement techniques, which are used in semiconductor manufacturing processes. More specifically, the 15 present invention relates to overlay marks for measuring alignment error between different layers or different patterns on the same layer of a semiconductor wafer stack.

The measurement of overlay error between successive patterned layers on a wafer is one of the most critical process control techniques used in the manufacturing 20 of integrated circuits and devices. Overlay accuracy generally pertains to the determination of how accurately a first patterned layer aligns with respect to a second patterned layer disposed above or below it and to the determination of how accurately a first pattern aligns with respect to a second pattern disposed on the same layer. Presently, overlay measurements are performed via test patterns that are printed 25 together with layers of the wafer. The images of these test patterns are captured via an imaging tool and an analysis algorithm is used to calculate the relative displacement of the patterns from the captured images.

The most commonly used overlay target pattern is the “Box-in-Box” target, 30 which includes a pair of concentric squares (or boxes) that are formed on successive layers of the wafer. The overlay error is generally determined by comparing the position of one square relative to another square.

To facilitate discussion, Fig. 1A is a top view of a typical “Box-in-Box” target  
10. As shown, the target 10 includes an inner box 12 disposed within an open-  
centered outer box 14. The inner box 12 is printed on the top layer of the wafer while  
the outer box 14 is printed on the layer directly below the top layer of the wafer. As is  
5 generally well known, the overlay error between the two boxes, along the x-axis for  
example, is determined by calculating the locations of the edges of lines c1 and c2 of  
the outer box 14, and the edge locations of the lines c3 and c4 of the inner box 12, and  
then comparing the average separation between lines c1 and c3 with the average  
separation between lines c2 and c4. Half of the difference between the average  
10 separations c1&c3 and c2&c4 is the overlay error (along the x-axis). Thus, if the  
average spacing between lines c1 and c3 is the same as the average spacing between  
lines c2 and c4, the corresponding overlay error tends to be zero. Although not  
described, the overlay error between the two boxes along the y-axis may also be  
determined using the above technique.

15 There was also the introduction of the “Box in Bar” target and the “Bar in Bar”  
target, both of which had the same general appearance as the “Box in Box” target. In  
“Box in Bar” targets, the outer box of the “Box in Box” target is separated into a  
plurality of parallel bars (see Fig. 1B). In “Bar in Bar” overlay marks, both the outer  
20 and inner box of the “Box in Box” target are separated into a plurality of parallel bars.  
A further example of this type of modified target is taught by Chen et. al in U.S.  
Patent No. 6,118,185.

Recently, there was the introduction of separated bars that created features  
25 comparable to the design rules of the device itself. By way of example, Ausschnitt et  
al., in U.S. Pat. No. 6,130,750, discloses “Box-in Box” type targets having separated  
bars.

Although such designs have worked well, there are continuing efforts to  
30 provide targets with improved functionality. For example, it would be desirable to  
have targets capable of improving the correlation between the overlay error measured  
on the test pattern and the real overlay error of the circuit components.

## Summary of the Invention

The invention relates, in one embodiment, to an overlay mark for determining  
5 the relative shift between two or more successive layers of a substrate. The overlay  
mark includes at least one test pattern for determining the relative shift between a first  
and a second layer of the substrate in a first direction. The test pattern includes a first  
set of working zones and a second set of working zones. The first set of working  
zones are disposed on a first layer of the substrate and have at least two working  
10 zones diagonally opposed and spatially offset relative to one another. The second set  
of working zones are disposed on a second layer of the substrate and have at least two  
working zones diagonally opposed and spatially offset relative to one another. The  
first set of working zones are generally angled relative to the second set of working  
zones thus forming an “X” shaped test pattern.

15 The invention relates, in another embodiment, to an overlay mark for  
determining the relative shift between two or more successive layers of a substrate via  
an imaging device configured for capturing an image of the overlay mark. The  
overlay mark includes a first set of working zones disposed on a first layer of the  
substrate. The first set of working zones includes at least two working zones  
20 diagonally opposed to one another and positioned within the perimeter of the mark.  
Each of the working zones includes a periodic structure of coarsely segmented  
elements positioned therein. The coarsely segmented elements are generally oriented  
in a first direction. The overlay mark further includes a second set of working zones  
25 positioned crosswise relative to the first working group. The second working group is  
disposed on a second layer of the substrate and has at least two working zones  
diagonally opposed to one another and positioned within the perimeter of the mark.  
Each of the working zones has a periodic structure of coarsely segmented elements  
30 positioned therein. The coarsely segmented elements are generally oriented in the  
first direction.

The invention relates, in another embodiment, to an overlay mark for  
determining the relative shift between two or more separately generated patterns on a  
single layer of a substrate. The overlay target includes a test region positioned on a

first layer of the substrate. The first layer is typically formed by a first pattern via a first process and a second pattern via a second process. The overlay target further includes a plurality of working zones positioned in the test region. The working zones representing the actual areas of the test region that are used to determine the relative shift between the first and second patterns. A first portion of the working zones are formed via the first process and a second portion of the working zones are formed via the second process. The overlay target additionally includes a periodic structure positioned within each of the working zones. Each of the periodic structures includes a plurality of coarsely segmented elements. Each of the coarsely segmented elements are formed by a plurality of finely segmented elements.

The invention relates, in another embodiment, to a method for determining the relative shift between two or more successive layers of a substrate or between two or more separately generated patterns on a single layer of a substrate. The method includes capturing an image of an overlay mark formed on the substrate. The overlay mark having a plurality of working zones. Each of the working zones including a periodic structure of coarsely segmented elements. The method further includes selecting a plurality of working zones from the captured image, wherein at least one working zone from each layer or pattern is selected. The method additionally includes forming representative signals for each of the selected working zones, wherein at least one signal for each layer or pattern is formed. The method also includes comparing the signal from the first layer or pattern to the signal from a second layer or pattern to determine the relative shift between different layers or patterns.

**Brief Description of the Drawings**

The present invention is illustrated by way of example, and not by way of  
5 limitation.

Figs. 1A&B are top plan views of overlay marks, which are well known in the  
art.

10 Fig. 2 is a top plan view of an overlay mark, in accordance with one  
embodiment of the present invention.

Fig. 3 is a partial side elevation view of a finely segmented periodic structure,  
in accordance with one embodiment of the invention.

15 Fig. 4 is a partial side elevation view of a finely segmented periodic structure,  
in accordance with one embodiment of the invention.

20 Fig. 5 is a partial side elevation view of a finely segmented periodic structure,  
in accordance with one embodiment of the invention.

Fig. 6 is a top plan view of an overlay mark, in accordance with an alternate  
embodiment of the present invention.

25 Fig. 7 is a top plan view of an overlay mark, in accordance with an alternate  
embodiment of the present invention.

Fig. 8 is a top plan view of an overlay mark, in accordance with an alternate  
embodiment of the present invention.

30 Fig. 9 is a top plan view of an overlay mark, in accordance with an alternate  
embodiment of the present invention.

Fig. 10 is a top plan view of an overlay mark, in accordance with an alternate

embodiment of the present invention.

Fig. 11 is a top plan view of an overlay mark, in accordance with an alternate embodiment of the present invention.

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Fig. 12 is a top plan view of an overlay mark, in accordance with an alternate embodiment of the present invention.

10 Fig. 13 is a top plan view of an overlay mark, in accordance with an alternate embodiment of the present invention.

Fig. 14 is a top plan view of an overlay mark, in accordance with an alternate embodiment of the present invention.

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Fig. 15 is a top plan view of an overlay mark, in accordance with an alternate embodiment of the present invention.

Fig. 16 is a top plan view of an overlay mark, in accordance with an alternate embodiment of the present invention.

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Fig. 17 is a top plan view of an overlay mark, in accordance with an alternate embodiment of the present invention.

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Fig. 19 is a simplified diagram of an overlay measurement system, in accordance with one embodiment of the present invention.

Fig. 20A is a simplified flow diagram illustrating a method of calculating overlay, in accordance with one embodiment of the present invention.

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Fig. 20B is a top plan view of the overlay mark of Fig. 9, in accordance with one embodiment of the present invention.

Fig. 20C illustrates a pair of collapsed 1D signals, in accordance with one embodiment of the present invention.

Fig. 21 is a flow diagram illustrating a method 370 of calculating overlay using Covariance, in accordance with one embodiment of the present invention.

5 Fig. 22 is a flow diagram illustrating a method 380 of calculating overlay using Fourier Decomposition, in accordance with one embodiment of the present invention.

10 Fig. 23 is a flow diagram of a method of designing an overlay mark, in accordance with one embodiment of the present invention.

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## Detailed Description of the Invention

The present invention will now be described in detail with reference to a few  
5 preferred embodiments thereof as illustrated in the accompanying drawings. In the following description, numerous specific details are set forth in order to provide a thorough understanding of the present invention. It will be apparent, however, to one skilled in the art, that the present invention may be practiced without some or all of these specific details. In other instances, well known process steps have not been  
10 described in detail in order not to unnecessarily obscure the present invention.

The present invention, in each of the various embodiments, uses overlay marks that are composed of periodic structures formed on each of two layers of a semiconductor wafer to provide overlay information between those two layers of the  
15 semiconductor device. The overlay marks are formed in specific locations on each wafer layer such that the periodic structures on one layer will be aligned with the periodic structures on the other layer when the two layers are properly aligned. Conversely, the periodic structures on each layer will be offset from each other when the two layers are not properly aligned. Alternatively, the present invention may use  
20 overlay marks that are composed of periodic structures formed on a single layer by two or more separate processes to provide alignment information between two different patterns on the same layer. Each of the periodic structures is composed of a plurality of structures, which increases the amount of information that may be used to measure overlay, and which may be widely modified to diminish the impact of certain  
25 processes on the overlay measurements. Each of these structures is composed of sub-structures that are about the same size and pitch (e.g., separation) as structures of the actual integrated circuits. By forming each of the periodic structures with sub-structures that are sized closer to the size of the actual circuits, a more accurate measurement of any alignment error in such circuits is obtained. The invention is  
30 particularly suitable for overlay measurement techniques that require capturing an image of the overlay mark.

The periodic structures and sub-structures described herein are generally patterned using suitable photolithographic techniques, and the lithographic patterns

are subsequently transferred to other materials and layers using established processing techniques such as etching and deposition. In the simplest application, the transferred patterns constitute etched or deposited lines or vias. For example, the periodic structures and sub-structures may be formations of photoresist material, recessed cavity formations, embedded trenches and/or other structures within a wafer layer.

5      The structures and sub-structures formed by cavities may be cavities formed in any of the layers during the semiconductor fabrication process. For example, the cavities may be formed in the photoresist layer, the dielectric material layer, or the metal layers. It should be noted that the above processes are not a limitation and that any

10     suitable fabrication technique may be used.

Embodiments of the invention are discussed below with reference to Figs. 1 – 23. However, those skilled in the art will readily appreciate that the detailed description given herein with respect to these figures is for explanatory purposes as

15     the invention extends beyond these limited embodiments.

Fig. 2 is a top plan view of an overlay mark 70, in accordance with one embodiment of the present invention. The mark 70 is suitable for image based overlay measurement techniques. For ease of discussion, overlay mark 70 is shown in a configuration that results when the tested layers of a wafer are in proper alignment. The overlay mark 70 is generally provided to determine the relative shift between two or more successive layers of a substrate or between two or more separately generated patterns on a single layer of a substrate. By way of example, the overlay mark may be used to determine how accurately a first layer aligns with respect to a second layer

20     disposed above or below it or how accurately a first pattern aligns relative to a preceding or succeeding second pattern disposed on the same layer. For ease of discussion, the overlay mark shown in Fig. 2 will be described in context of measuring overlay between different layers of a wafer. It should be noted, however,

25     that the overlay marks in this figure (and any subsequent figures) may also be used to measure two or more separately generated patterns on a single layer of a wafer. In general, any convenient orientation of the mark relative to the placement of the dies on the wafer can be chosen so long as the orientation of the successive marks is the

30     same from layer to layer or from pattern to pattern.

The overlay mark 70 is defined by a perimeter 71. The perimeter 71 is typically based on metrology tool limitations and circuit design rules. For instance, the upper limits of the perimeter may be set by the field of view (FOV) of the metrology tool used to measure overlay and/or the scribe line budget. The FOV 5 generally refers to the optical perimeter that defines the area available for capturing an image via the metrology tool. The mark is typically positioned inside the scribe line of the wafer, i.e., the scribe line is the place on the wafer where the wafer is separated into dies via sawing or dicing and thus the circuit itself is not patterned there. The scribe line budget, therefore, generally refers to the available space allowed by the 10 scribe line for the placement of the mark. In addition, the lower limits of the perimeter may be set by the minimum area needed by the metrology tool to image the mark (e.g., obtain adequate signal or measurement quality).

It is generally believed that the perimeter 71 should be as a large as possible so 15 as to maximize the amount of information used for overlay measurements. The size and shape of the perimeter 71 may be widely varied. For example, the perimeter may form shapes such as squares circles, triangles, rectangles, polygons and the like. The size of the perimeter, in any given direction, is generally between about 10 and about 100 microns, and more particularly between about 20 and about 50 microns. In the 20 illustrated embodiment, the perimeter 71 directly corresponds to the size and shape of the FOV 79 of the metrology tool, i.e., the FOV defines the perimeter. In most cases, the FOV is rectangular shaped due to CCD proportions. It should be noted, however, that this is not a limitation and that the FOV may be substantially larger than the 25 mark's perimeter 71. For example, the mark's perimeter may be limited by the area on the scribe line.

The overlay mark 70 further includes a plurality of working zones 72, which 30 are configured to divide and substantially fill the perimeter 71 of the mark 70 (from center to edge). For example, the working zones may be configured to fill the perimeter of the mark such that the combined area of the working zones is substantially equal to the total area of the mark. The working zones 72 represent the actual areas of the mark that are used to calculate alignment between different layers of the wafer. As such, the working zones 72 typically include information relating to the two layers for which overlay measurements are made. For example, some of the

working zones are positioned in one layer of the wafer (represented by solid lines) while some of the working zones are positioned in a different layer of the wafer (represented by dashed lines).

5        In most cases, the working zones 72 are spatially separated from one another so that they do not overlap portions of an adjacent working zone (i.e., each of the working zones represents a different area of the mark). This is typically done to ensure that each of the working zones is properly imaged by the metrology tool. Although not always necessary, the working zones 72 may be spatially separated by  
10      an exclusion zone 80 so that each of the working zones is distinct. Exclusion zones 80 are areas of the target image where either a physical target structure or a corresponding optical signal is distorted and therefore it is excluded from the overlay calculation. The size of the exclusion zones is typically balanced with the size of the working zones so as to provide as much information as possible for the measurement  
15      of overlay. That is, it is generally desired to have larger working zones and smaller exclusion zones. In some cases, it may be desirable to have a small amount of overlap between adjacent working zones so as to allow SEM cross sectioning to verify the accuracy of the overlay measurements.

20        In one embodiment, the geometry, including size, shape and distribution of the working zones is configured to balance out or compensate for non-uniform or asymmetrical characteristics that may occur across the mark. Asymmetries may arise from the optical aberration and illumination asymmetry in the metrology tool (tool induced shifts), as well as process induced structural features (wafer induced shifts).

25        Tool induced shift (TIS) generally refers to how much the apparent position of the mark moves or shifts as a result of metrology tool problems such as non –uniform illuminations and/or optical aberrations. Illumination generally pertains to how the light is distributed to the target. Aberrations generally pertain to how the light is focussed and collected. Non-uniform illuminations may be caused by a defect or misalignment in one of the optical components associated with transferring the light from the light source to the wafer. Non-uniform optical aberrations may be caused by a defect or misalignment in the objective lens of the optical system. By way of example, aberrations may include spherical aberrations, astigmatism aberrations and

coma aberrations. Coma aberrations may have a greater impact on TIS as a result of its asymmetrical nature across the FOV of the metrology tool,

5       Wafer induced shift (WIS) generally refers to how much the apparent position  
of the mark moves as a result of process variations such as distortions caused by  
chemical mechanical polishing (CMP) and sputter deposition.

10      Non-uniformities, asymmetries and variations may also arise from difference  
between the height of the different line sets from each layer. The lower layer lines are  
sometimes visible only through the intermediate layer of the wafer since the  
intermediate layer material covers the lines on the lower layer. On the other hand, the  
upper lines are generally formed from the photoresist applied on the top of the lower  
layer.

15      Accordingly, by adjusting the size, shape and distribution of the overlay  
information from the two layers or patterns within the field of view of the overlay  
metrology tool, it is possible to diminish the impact of lithography and/or process  
non-uniformities, asymmetries and variations. In some cases, it may even be possible  
to enhance the overlay measurement.. Resist patterns are less inclined to process  
variation and thus they may potentially be more useful for acquisition areas of the  
mark.

25      In one implementation, balancing is accomplished by selectively positioning  
the working zones of the same layer at different positions around the perimeter of the  
mark. For example, the working zones may be positioned at different positions within  
the FOV in order to get the best possible balance between aberrations and  
illumination and process results that vary across the FOV. In the illustrated  
embodiment (Fig. 2), working zones of the same layer are positioned opposite one  
another so as to balance out asymmetries that may occur from the left to the right or  
30     from the top to the bottom or from the inner to the outer regions of the FOV (or vice  
versa). In one implementation, the optimal distribution for balancing out asymmetries  
is determined by performing optical simulations of tool induced shift (e.g., how much  
the apparent position of the mark moves as a result of tool problems such as  
illumination and/or aberrations). In another implementation, the optimal distribution

for balancing out asymmetries is determined by performing experiments (e.g., run a sample of wafers). In addition, within the FOV there might be parts of the mark, which have unacceptable aberrations and illuminations and/or process damage, and therefore, the working zones may be positioned at specific positions inside the FOV to avoid these areas.

As mentioned, each of the working zones is configured to represent one of the two successive layers of the overlay mark. In one embodiment, the working zones represent an equal number of first layers or patterns and second layers or patterns, i.e., for each working zone in the first layer there is a corresponding working zone in the second layer. This is typically done to balance variations, non-uniformites and/or asymmetries that may exist in the layers and/or the metrology tool. As such, the number of working zones is generally based on a factor of 2, as for example, 2, 4, 8, 16 and the like. It is generally believed that by distributing the regions to more points within the field of view, the more likely they are to balance out the non-uniformities caused by illuminations, aberrations and the process. By way of example, the size of the working zones (e.g., square) is generally between about 2 to about 24 microns, and more particularly between about 4 to about 15 microns. In most cases, the size of the working zones is inversely proportional to the number of working zones, i.e., as the number increases, the size decreases. By way of example, the size of the working zones is generally between about 10 and about 24 microns for four working zones, between about 5 and about 12 microns for eight working zones, and between about 2.5 and about 6 microns for sixteen working zones.

In addition, there may be cases that require an uneven number of first and second layered working zones, i.e., 4 first layered working zones and 2 second layered working zones. There may also be the case that requires an uneven number of total working zones, i.e., 2 first layered working zones and 1 second layered working zones. There may also be the case that requires unequal working zone sizes. For example, a first group of working zones may have a first size while a second group of working zones may have a second size where the second size is either smaller or larger than the first size.

Although the working zones are generally constrained by the FOV (e.g., perimeter of the mark), the shape of the working zones may vary according to the specific needs of each mark. By way of example, the zones may have a square shape (as shown), an L shape, a rectangular shape, a triangular shape, a circular shape, a 5 polygonal shape and the like. In most cases, the shape and size of the working zones are identical. This is typically done to balance variations, non-uniformities and/or asymmetries that may exist in the layers and/or the metrology tool. However, it should be noted, that this is not a limitation and that some or all of the working zones may have different shapes. For example, some of the working zones may have a 10 rectangular shape while other working zones may have a square shape.

Furthermore, working zones representing different layers are typically juxtaposed relative to one another. By way of example, the mark may include at least two juxtaposed working zones: a right region representing a first layer and a left 15 region representing a second layer. In addition, the mark may include a top working zone representing a first layer and a bottom working zone representing a second layer. In some implementations, the juxtaposed regions are positioned equidistant from the center of the target (e.g., center of FOV). It should be noted, however, that 20 juxtaposition is not a limitation and that the position of the working zones may vary according to the specific needs of each mark. For example, there may be cases that require working zones representing first layers to be juxtaposed relative to other working zones representing first layers (or vice versa).

Referring to Fig. 2, working zones 72A and 72D (represented by dashed lines) 25 are formed in one layer of the wafer while working zones 72B and 72C are formed in a different layer of the wafer (represented by solid lines). As shown, working zones 72A&D are angled relative to working zones 72B&C. That is, working zones 72A&D lie crosswise relative working zones 72B&C. Furthermore working zones 72A and 72D, which are disposed on the same first layer, are positioned opposite one 30 another at a first vertical angle while working zones 72B and 72C, which are disposed on the same second layer, are positioned opposite one another at a second vertical angle. That is, working zone 72A is diagonally opposed to working zone 72D, and working zone 72B is diagonally opposed to working zone 72C. Moreover, working zone 72A is spatially offset from working zone 72D, and working zone 72B is

spatially offset from working zone 72D. For example, the center of working zone 72D is positioned below and to the right of the center of working zone 72A, and the center of working zone 72C is positioned below and to the left of the center of working zone 72B. As should be appreciated, these cross-positioned structures form

5 an "X" shaped pattern.

It should be noted that this particular "X" configuration is shown by way of example and not by way of limitation, i.e., the size, shape and distribution of the working zones and their periodic structures may vary according to the specific needs

10 of each mark. For example, the working zones may be configured to fill a variety of different sized and different shaped FOVs. It is generally desirable to fill the field of view with as much information as possible for reasons of process robustness and information optimization. The working zones may also be configured to take on other shapes such as rectangles, triangles, parallelograms, trapezoids, regular polygons,

15 circles and the like. Furthermore, the opposing periodic structures may be disposed on other layers. For example, working zones 72B and 72C may be disposed on the first layer (dashed lines) and working zones 72A and 72D may be disposed on the second layer (solid lines). Further still, the working zones may only partially fill the field of view. Moreover, the exclusion zones may be eliminated so that the working zones

20 may be positioned next to one another along their edges (e.g., completely filling the regions and thus the FOV) or partially over one another so as to allow for line ends to overlap for cross section accuracy.

Each of the working zones 72 contains an individual periodic structure 74, as

25 for example, periodic structures 74A-D. As shown, each of the periodic structures 74 substantially fills the perimeter of its corresponding working zone 72. Moreover, each of the periodic structures 74 includes a plurality of coarsely segmented lines 76 that increase the amount of information that may be used for overlay measurements. In addition, by constructing marks from periodic structures, it is possible to implement a

30 broader range of overlay measurement algorithms that maximize the benefits of higher information density in the mark. Each of the coarsely segmented lines 76 is formed by a number of sub-structures or finely segmented elements 78.

Even though some of the finely segmented elements 78 may be represented by dashed lines, the finely segmented elements 78 within each periodic structure are not necessarily discontinuous linear formations that are segmented at regular intervals.

The dashed lines may represent continuous linear formations within each of the 5 periodic structures. However, in alternative embodiments, it is possible that the finely segmented elements 78 within each periodic structure may take on various shapes and sizes, which include discontinuous linear formations that are segmented at regular intervals. These will be described in greater detail below.

10        In one embodiment, the geometry, i.e., linewidth and spacing, of the periodic structure is configured to find the proper balance between the image resolution of the metrology tool and the robustness of the process. For instance, in most cases, it is desirable to have a large geometry (e.g., large linewidths and spacings) so that the periodic structure may be optically resolved by the tool, and a small geometry (e.g., 15 small linewidths and spacings) so that the process effects on the mark are minimized. With regards to image resolution, there is a minimum size requirement that each metrology tool has for resolving the coarsely segmented lines. Furthermore, it is generally known that as the period of the periodic structure gets smaller, the metrology tool resolution diminishes, i.e., there is a point where the resolution of the 20 metrology tool ceases to work effectively. With regards to process robustness, each time a new process is introduced in semiconductor manufacture, there is some impact on the overlay mark. The ability to measure the target depends on it's visibility or contrast in the image tool. Some processes such as metallization tend to diminish contrast, hence impacting precision. Other processes such as chemical mechanical 25 polishing (CMP) tend to blur or distort the mark, hence impacting accuracy. These processes may also make the structures asymmetric or create an apparent, optically measured spatial translation of the center of the structures relative to the center of the originally patterned trench or line (e.g., circuit pattern).

30        For specific processes, such as aluminum coated, chemically mechanically polished tungsten, it is advantageous for the characteristic dimensions of these structures to be approximately 1 to 2 microns or less in order to diminish the impact of asymmetries resultant from the polishing and aluminum deposition processes. However, if the width of the trench is too small, the remaining topography at the top

of the aluminum layer is too small to provide optically adequate contrast and thus the mark does not provide adequate overlay information. On the other hand, the lower bound for characteristic dimensions of structures at this scale is determined by the resolution limit of the metrology tool. For example for an overlay tool with a

5 numerical aperture (NA) of 0.9 and a mean illumination wavelength of 550 nm gives a Rayleigh resolution limit or criteria of approximately 0.4 microns. In this particular case, it may be preferable to maintain the linewidth above 0.5 microns in order not to diminish contrast and hence signal to noise, and below 1-2 micron in order to diminish the impact of asymmetries resultant from the polishing and aluminum

10 deposition processes. It should be noted , however, that this is by way of example and not by way of limitation and that it may be possible to achieve better than the Rayleigh resolution limit.

In one embodiment, the geometry of the periodic structure is determined by

15 experimentation. For example, several wafers may be run through a process to find a period for which the TIS variability is the smallest, i.e., measure the tool induced shift variability at multiple sites on the wafer and then select the pitch that minimizes the TIS variability and process variability.

20 In one embodiment, the period and phase of the periodic structure is configured to filter out high frequency edges.

In the illustrated embodiment, each of the periodic structures 74A-D has the same pitch and duty cycle. That is, each of the periodic structures 74 consist of an

25 equal number of coarsely segmented lines 76, which are parallel and which have equal linewidths and equal spacings therebetween. The dimensions of the pitch, linewidths and spacings may be widely varied. By way of example, the dimension of the pitch may be between about 1 to about 3 microns, and the dimensions of the linewidths and spacings may be between about 0.3 to about 2 microns, and more

30 particularly between about 0.5 to about 1 micron.

It should be noted that equal pitch, linewidths and spacings for each of the periodic structures is not a limitation and that they may vary according to the specific needs of each mark. For example, each of the periodic structures may have a different

pitch or duty cycle. Alternatively, some of periodic structures may have the same pitch or duty cycle, while other periodic structures may have a different pitch or duty cycle. Furthermore, the periodic structures may have a pitch that varies across the periodic structure. By way of example, the periodic structure may be a chirped  
5 periodic structure (e.g., smaller to larger). As was explained previously, the pitch, linewidths and spacings are generally optimized according to process robustness requirements and contrast requirements of the metrology tool.

The number of lines inside each periodic structure may be varied to meet the  
10 specific needs of each mark. It is generally believed that the number of lines is dependent on the resolution required and the signal to noise ratio desired. Most imaging tools have a resolution limit between about 0.3 and about 0.9. The number of lines may also be determined by process requirements such as chemical mechanical polishing distortions that affect the outermost line segments more than the inner line  
15 segments of a group. One factor affecting the maximum number of lines that may be used within a group of line segments is the metrology tool resolution. From the perspective of the minimum number of lines that is needed for operation, that number is two. In the embodiment shown, each of the periodic structures 74A-D includes 5 coarsely segmented lines. In some cases, it may even be desirable to have periodic  
20 structures having a different number of lines, i.e., a first periodic structure having 5 lines and a second periodic structure having 2 lines.

In the embodiment shown, the lines of the periodic structures 74A-D are parallel to one another so as to provide position information in a single direction. As  
25 should be appreciated, the lines in Fig. 1 are configured for X-axis measurements since the lines are non-parallel (e.g., perpendicular or orthogonal) to the axis of measurement. Given this configuration, any offset between the two successive layers in the X-direction will be present between the first set of periodic structures 74 A&D and the second set of periodic structures 74 B&C. As such, the alignment between  
30 the two layers of the wafer in the X-direction may be determined by comparing the relative positions of the two groups of periodic structures. For instance, the positions of periodic structures 74 A and D, which are disposed on the first layer, may be compared with the positions (e.g., centers of symmetry) of periodic structures 74 B

and C, which are disposed on the second layer, to determine the alignment between consecutive layers in the X direction.

In one embodiment, the overlay alignment between layers is determined by calculating the centers of symmetry for each of the opposing zones on the same layer, and then calculating the difference between the two averaged centers of symmetry. For example, the center of symmetry for both working zones 72A and D and working zones 72B and C may be found by folding the images over, and the difference of these two centers of symmetry may determine the overlay error. If there is zero overlay, the centers of symmetry of each of the two opposing groups on the same layer should coincide with the Y axis that runs through the middle of the mark, i.e., between the left working zones and the right working zones.

Alternatively, periodic structures that are lateral to one another and on different layers are compared, i.e., periodic structure 74A is compared to periodic structure 74B and periodic structure 74C is compared to periodic structure 74D. In addition, periodic structures that are above or below one another are compared, i.e., periodic structure 74A is compared to periodic structure 74C and periodic structure 74B is compared to periodic structure 74D.

It should be noted that measuring overlay in the X-direction is not a limitation and that the overlay mark 70 may be rotated 90 degrees to determine the registration error between the two layers of the wafer in the Y-direction. Furthermore, two overlay marks, one of which is rotated 90 degrees from the other, can be used to determine the alignment between consecutive layers in two directions, as for example in the X and Y directions. The second mark may be positioned at various locations relative to the first mark so long as the orientation of the first and second marks are the same layer to layer (e.g., side by side or at different locations on the wafer if space is limited).

Note additionally, that if the lines shown in solid outline are printed on the first layer of the semiconductor wafer with the lines shown solid on the second layer, then on the third layer another set of lines (shown here in solid outline) are printed over, and covering, the lines of the first layer. Then the lines of the second layer are

used in conjunction with lines on the third layer. Thus, each set of lines on a layer of the semiconductor wafer (except for those on the first and last layers) are used in conjunction with the lines on two layers of the semiconductor wafer, the one below and the one above. This implementation works best if the first layer cannot be

- 5 detected optically when below the third layer. Alternatively, if there is sufficient space on the semiconductor wafer surface, the grating pairs for each pair of adjacent layers on the wafer could be in a different location on the wafer to minimize any "bleed through" interference from a third layer on the measurement for the top two layers of interest.

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To elaborate further, finely segmented elements 78, which are used to form each of the coarsely segmented lines 76, are configured to allow the overlay mark 70 to facilitate overlay measurements that more accurately represent the degree of alignment between the wafer layers. That is, the finely segmented elements 78 serve 15 to provide alignment information that more closely matches the alignment of the patterns of the integrated circuits that are formed on each of the two layers. The finely segmented elements 78 allow for more representative measurements, in part, due to several reasons.

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One reason for which smaller overlay marks provide more accurate overlay measurements is that the smaller sized finely segmented elements are formed on the semiconductor layer with lens pattern placement errors that are more similar to the lens pattern placement errors with which the patterns for the integrated circuits are formed. Patterns are formed on wafer layers with lithographic devices such as 25 "steppers." The lens placement errors of patterns formed upon a semiconductor wafer change with the size and spacing of the patterns due to aberrations within the stepper lenses, and with the illumination conditions (including off-axis illumination and partial coherence) used to expose the circuit pattern defined on the lithographic mask. Creating marks having feature size and pitch more comparable to that of the 30 integrated circuit element critical dimensions, as well as using the same or similar mask pattern techniques as the circuit features (e.g., using the same or similar optical proximity correction or phase shift mask patterns), results in mark and integrated circuit patterns that are formed with a more similar degree of lens pattern placement errors. In this manner, the alignment between marks on different layers of a wafer

gives a more accurate indication of the alignment between the circuit patterns. For more information regarding distortions due to stepper lens aberrations, see Lithography Process Control, by Harry J. Levinson.

5        In one embodiment, the feature size and pitch (e.g., the distance between the centers of the finely segmented elements of the finely segmented elements are substantially equal to those of the critical device features of the patterning step performed on the layers under test. That is, the dimensions of the finely segmented elements 78 are comparable to the dimensions of the circuit patterns. In one  
10 implementation, the line has a width that is approximately equal to the width of an integrated circuit interconnection line. Currently, circuit interconnection lines have widths that are approximately equal to or less than 0.13 um. The finely segmented elements of the current invention can be made to have widths as small as 0.05 - 0.2 um. However, as can be appreciated, advances in semiconductor manufacturing  
15 processes are likely to further reduce these dimensions and therefore these dimensions are by way of example and not by way of limitation.

Another reason for which smaller overlay marks provide more accurate overlay measurements is that effects of wafer fabrication asymmetries on overlay measurement may be reduced. Wafer fabrication asymmetries are shifts in the shape and size of structures or patterns that have been formed upon a wafer layer due to further fabrication processes. The effects of wafer fabrication processes on overlay marks depends on the size, spacing and density of the overlay mark structures and substructures. These shifts in shape and size affect the overlay marks such that  
25 accuracy of the overlay measurements may be deteriorated.

An exemplary wafer fabrication technique that may cause wafer structures to gain asymmetrical profiles is the sputter deposition process. The sputtering process is generally used to apply a layer of material (i.e., metal) on top of an existing wafer  
30 layer. Usually, the source of the sputtered material, a target, is located above the center of the wafer. The sputtered material travels at an angle from the target towards the outer perimeter of a wafer thereby resulting in asymmetrical deposition of material within recessed channels or over ridge-like protrusions. Specifically, the unequal accumulation of deposited material between the sidewalls of a recessed channel may

cause an apparent positional shift of the recessed channel towards one side of the channel.

Another exemplary fabrication technique that may cause asymmetrical dimensions is the chemical mechanical planarization (CMP) of wafer layers. In certain circumstances, wafer layers undergo CMP before the next layer of material is deposited. The CMP device generally travels over a wafer layer in a specific direction. The CMP device, therefore, will first encounter one side of an overlay mark and then run down the opposite side of the mark. This results in a shift and change in the apparent size of the overlay mark since the material on the side of the overlay mark which is encountered first may be removed to a greater or lesser degree than the opposite side of the mark.

In both situations, the resulting asymmetries to overlay marks due to the fabrication processes may be reduced by forming smaller marks. With respect to the sputtering process, smaller recessed channels or ridges allows less sputtered material to accumulate on the respective side surfaces, thereby resulting in a smaller asymmetrical shift in shape and size. With respect to the CMP process, marks having smaller dimensions will also be shifted to a lesser degree. Conversely, there may be process situations, where widening the lines of the overlay marks may make them more robust to process variation, as for example, in cases of metal layers with large grain size. Refer to *Lithography Process Control*, by Harry J. Levinson, for further information on wafer fabrication asymmetries.

In the illustrated embodiment, the finely segmented elements 78 appear as thin lines that are spatially separated and parallel to each other. It should be noted, however, that lines are not a limitation and that the shape of the finely segmented elements may vary according to the specific needs of each mark. For example, the finely segmented elements may be composed of squares, rectangles, triangles, polygons, circles, ovals and the like. As should be appreciated, the finely segmented elements 78 may not have perfectly symmetrical shapes since they are typically formed via lithographic and pattern transfer processes.

Further variations include variously shaped elements that are formed within a single overlay mark. For example, one periodic structure may contain linearly shaped elements while a different periodic structure may contain circularly shaped elements. In addition, one periodic structure may contain circularly shaped elements and a different periodic structure may contain square shaped elements. Moreover, one periodic structure may contain linearly shaped elements and a different periodic structure may contain square shaped elements. Even further variations include variously shaped elements that are formed within a single periodic structure. For example, a single periodic structure may include one coarsely segmented line that is formed by linearly shaped elements and another coarsely segmented line that is formed by square shaped elements. Even further variations, include some periodic structures which are composed of finely segmented elements and others which are not composed of finely segmented elements, but rather single solid lines.

Referring to Fig. 3, the finely segmented elements 78 will be described in greater detail. Fig. 3 is a partial side elevation view of any one of the periodic structures 74 shown in Fig. 2, in accordance with one embodiment of the invention. As shown, the coarsely segmented lines 76 are formed by a plurality of finely segmented elements 78. In this particular embodiment, the finely segmented elements 78 represent bars, which are symmetrically distributed relative to the center of the coarsely segmented line 76, and which having equal fine widths,  $w$ , and fine pitch,  $p$  (distance between centers), therebetween. It should be noted, however, that this is not a limitation and that the widths and pitch, as well as the distribution, may vary according to the specific needs of each device. The linewidth,  $d$ , of the coarsely segmented lines 76 is defined as the distance between the outer edges of the very first and the very last bar 78' and 78" of the plurality of finely segmented bars 78. In the illustrated embodiment, there are 10 finely segmented bars.

It has been found that stepper aberrations (not metrology tool aberrations) such as coma may cause an apparent overlay error in cases where the overlay mark is finely segmented. That is, in addition to having the tendency to cause pattern placement errors, stepper coma may also have the tendency to modify the dimensions of the finely segmented elements, in particular, the first and last bars that make up the coarsely segmented line (e.g., bars 78' and 78"). By "modify the dimensions", it is

generally meant that the first and last bars may become thinner or wider. In most cases, when one bar becomes wider the other bar becomes thinner. For example, coma may cause the last bar to become wider and the first bar to become thinner. This may also be caused by the proximity of the bars to the open space between coarsely  
5 segmented lines. As should be appreciated, this type of modification tends to introduce an apparent overlay shift (i.e., the line appears to shift from the left to the right) and thus the line may not be measured correctly. In one embodiment, the layout of the periodic structures may be reconfigured to compensate for this apparent shift (see Figs. 4 & 5).

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In lithography, a clear field generally refers to a series of periodic structures that are surrounded by an open space (e.g., etched) and a dark field generally refers to a series of periodic structures that are surrounded by a closed space (e.g., not etched). The clear fields generally appear brighter and the dark fields generally appear darker.  
15 In this particular embodiment, the clear field is a clearing between a group of finely segmented elements such as the bars of Fig. 3, and the dark field is a closed space between a group of finely segmented elements such as the bars of Fig. 3. It is generally believed that the clear fields and dark fields may be configured to alter the formation of the lines in such a way that the apparent shift, which is caused by the wider and thinner bars, cancel or balance out. In one implementation, the balancing is  
20 done within each periodic structure. For example, each of the working zones includes fine segmentation that comprises both a clear field and a dark field. For example, some of the coarsely segmented lines have clear fields and some have dark fields. In another implementation, the balancing is done between two periodic structures of  
25 different working zones. For example, at least a first working zone includes a periodic structure with fine segmentation that comprises a clear field and at least a second working zone includes a periodic structure with fine segmentation that comprises a dark field. In most cases, the first and second working zones are opposed working zones that are on the same layer. These implementations will be described in  
30 greater detail below with reference to Figs. 4 and 5.

Fig. 4 is a partial side elevation view of any one of the periodic structures 74 shown in Fig. 2, in accordance with one embodiment of the invention. By way of example, Fig. 4 generally corresponds to the first implementation described above.

As shown, the coarsely segmented lines 76 are formed by a plurality of finely segmented bars 78 and at least one dark field 81. The coarsely segmented lines 76 are separated by a separation, S, that includes a plurality of finely segmented bars 78 and at least one clear field 82. The geometry of the finely segmented lines, dark fields and clear fields may be widely varied. The geometry of these components generally depends on the partial coherence of illumination and coma aberrations of the optics of the stepper lens. Dual tone structures, in which both lines and spaces are partially segmented tend to represent better the pattern placement errors suffered by device structures, rather than structures comprising either one or the other.

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Figs. 5A&B are partial side elevation views of two distinct periodic structures 84 and 85, in accordance with one embodiment of the invention. In most cases, the periodic structures 84 and 85 represent periodic structures, which are from opposing working zones, and which are positioned in the same layer. For example, periodic structure 84 may correspond to periodic structure 74A while periodic structure 85 may correspond to periodic structure 72D. It should be noted, however, that this is not a limitation.

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Referring to Fig. 5A, the first periodic structure 84 includes coarsely segmented lines 76, which are formed by a plurality of finely segmented bars 78, and which are separated by clear fields 82. Referring to Fig. 5B, the second periodic structure 85 includes coarsely segmented lines 76, which are formed by a plurality of finely segmented bars 78, and which are separated by dark fields 81. The geometry of the finely segmented lines, dark fields and clear fields may be widely varied. In this particular embodiment, the size shape (although inverse) and position of the dark fields generally corresponds to the size shape (although inverse) and position of the clear fields.

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As can be seen from the foregoing, the advantages of the X-configuration are numerous. For instance, the X-configuration may provide more information than the standard box-in-box target by filling the perimeter of the mark from center to edge. It may also provide more information by increasing the number of edges (e.g., coarsely segmented lines) as well as their lengths. Apart from providing more information by increasing the number of edges and their lengths, the X-configuration exhibits further

advantage over the generic box-in- box target due to additional built-in symmetry. Namely, in the generic box-in-box structures there are inner and outer layers. Their swap would result in different pattern, and therefore – overlay result, due to different information distribution of inner and outer marks, and due to different optical  
5 behavior of the metrology tool in regions close to and far from the FOV center. From this point of view, the “X”-target structure is basically invariant to the layer swap (up to mirror transformation). Furthermore, the X configuration exhibits 180 degree rotational symmetry that helps to overcome anti-symmetrical coma patterns, i.e., coma will cancel out.

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Fig. 6 is a top plan view of an overlay mark 90, in accordance with an alternate embodiment of the invention. By way of example, the overlay mark 90 may generally correspond to the overlay mark shown in Fig. 2. Overlay mark 90 contains four working zones 92A-D for determining the registration error between two wafer  
15 layers (one layer is represented by cross-hatching, the other is not). Each of the working zones includes a periodic structure 94 comprised by a plurality of coarsely segmented lines 96. In a manner similar to the X target of Fig. 2, the periodic structures 94A and D positioned in the first and fourth zones 92 A and D are disposed in a first layer of the wafer while periodic structures 94 B and C positioned in the  
20 second and third zones 92 B and C are disposed in a second layer of the wafer. Furthermore, periodic structures on the same layer, as for example structures 94 A and D, are in diagonally opposed positions thereby forming an overlay mark with an X configuration.

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In this particular embodiment, the coarsely segmented lines are horizontally positioned and therefore they are configured to measure overlay in the Y-direction. It should be noted, however, that this is not a limitation and that the target may be rotated so as to measure overlay in the X-direction. Also in this embodiment, the coarsely segmented lines are solid structures that are elongated and rectangular in  
30 shape. It should also be noted that this is not a limitation and that the coarsely segmented lines may be formed by a plurality of finely segmented elements, which may be produced according to the finely segmented elements described in Fig. 4.

In contrast to the mark of Fig. 2, each of the working zones 92, and more particularly each of the periodic structures 94 in Fig. 6 has an “L shaped” outline or shape in order to accommodate an additional structure 96 in the center of the mark 90. In the illustrated embodiment, the additional structure 96 represents a standard box in 5 box structure as described in the background of this application. As such, the “X” configured mark 90 can be acquired and measured by standard box in box metrology tools and algorithms, i.e., existing equipment and software may be used.

Fig. 7 is a top plan view of an overlay mark 100, in accordance with an 10 alternate embodiment of the invention. By way of example, the overlay mark 100 may generally correspond to the overlay mark shown in Fig. 2. Overlay mark 100 contains four working zones 102A-D for determining the registration error between two wafer layers (one layer is represented by cross-hatching, the other is not). Each of the 15 working zones 102 includes a periodic structure 104 comprised by a plurality of coarsely segmented lines 106. In a manner similar to the “X” target of Fig. 2, the periodic structures 104A and D positioned in the first and fourth zones 102 A and D are disposed in a first layer of the wafer while periodic structures 104 B and C positioned in the second and third zones 102 B and C are disposed in a second layer 20 of the wafer. Furthermore, periodic structures on the same layer, as for example structures 104 A and D, are in diagonally opposed positions thereby forming an overlay mark with an X configuration.

In this particular embodiment, the coarsely segmented lines 106 are 25 horizontally positioned and therefore they are configured to measure overlay in the Y-direction. It should be noted, however, that this is not a limitation and that the mark 100 may be rotated so as to measure overlay in the X-direction. Also in this embodiment, the coarsely segmented lines are solid structures that are elongated and rectangular in shape. It should also be noted that this is not a limitation and that the coarsely segmented lines may be formed by a plurality of finely segmented elements, 30 which may be produced according to the finely segmented elements described in Fig. 4.

In contrast to the mark of Fig. 2, each of the working zones 102, and more particularly each of the periodic structures 104 in Fig. 7 has a “rectangular” outline or

shape in order to accommodate an additional structure 108 in the center of the mark 100. In the illustrated embodiment, the additional structure 108 represents a standard box in box structure as described in the background of this application. As such, the X configured mark 100 can be acquired and imaged by standard box in box overlay metrology tools and algorithms. It should be noted, however, that this is not a limitation and that the additional structure may represent other structures, as for example, a pattern recognition structure, which can be recognized and acquired by optical pattern recognition tools and algorithms. Both configurations have the advantage that no change is necessary to existing equipment and software.

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Fig. 8 is a top plan view of an overlay mark 110, in accordance with an alternate embodiment of the present invention. By way of example, overlay mark 110 may correspond to the mark of Fig. 2. It should be noted, however, that unlike the mark of Fig. 2, the overlay mark 110 is configured to measure overlay in two separate directions. As such, mark 110 obviates the need to have one mark for each direction in which overlay needs to be measured. Overlay mark 110 is shown in a configuration that results when the tested layers of a wafer are in perfect alignment.

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The overlay mark 110 includes a plurality of working zones 112 for determining the registration error between two wafer layers in two different directions. In the illustrated embodiment, the overlay mark 110 includes four square shaped working zones 112, which are configured to substantially fill a field of view (not shown) of the metrology tool used to image the overlay mark 110. The working zones 112 represent the actual areas of the mark that are used to calculate alignment between different layers of the wafer. As mentioned previously, the working zones 112 are spatially separated from one another so that they do not overlap portions of an adjacent working zone of the second layer.

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In this embodiment, the working zones are configured to provide overlay information in two directions, as for example, in the X and Y directions. Of the four working zones 112A-D, two of them 112A and D are disposed in the first layer and two of them 112 B and C are disposed in the second layer (the first layer is represented by solid fill, the second layer is represented by no fill). Working zones 112A and D, which are disposed on the same first layer, are positioned opposite one

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another at a first vertical angle while working zones 112B and 112C, which are disposed on the same second layer, are positioned opposite one another at a second vertical angle (e.g., diagonally). These cross-positioned structures form an “X” shaped pattern.

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Each of the working zones 112 contains an individual periodic structure 114, as for example, periodic structures 114A-D. As shown, each of the periodic structures 114 substantially fills the perimeter of its corresponding working zone 112. As should be appreciated, each of the periodic structures 114 are formed in the layer of its corresponding working zone 112. The periodic structures 114 include coarsely segmented elements 116 that are arranged in spaced apart rows and columns. Each of the coarsely segmented elements 116, in turn, are formed by finely segmented elements 118. The finely segmented elements 118 are also arranged in spaced apart rows and columns. The individual coarsely segmented elements 116 and finely segmented elements 118 may be configured with a variety of sizes, shapes and distributions. In the illustrated embodiment, both the coarsely segmented elements 116 and finely segmented elements 118 are square shaped and equally spaced from an adjacent element. As should be appreciated, overlay mark 110 can be used to measure the misregistration value in two separate directions that are perpendicular to each other since the mark 110 has the same repeating structural pattern in orthogonal directions.

Fig. 9 is a top plan view of an overlay mark 130, in accordance with an alternate embodiment of the invention. By way of example, overlay mark 130 may correspond to the mark of Fig. 2. It should be noted, however, that unlike the mark of Fig. 2, the overlay mark 130 of Fig. 8 is configured to measure overlay in two separate directions. As such, mark 130 obviates the need to have one mark for each direction in which overlay needs to be measured. Overlay mark 130 is shown in a configuration that results when the tested layers of a wafer are in perfect alignment. The overlay mark 130 is generally provided to determine the relative shift between two or more successive layers of a wafer or between two or more separately generated patterns on a single layer of a wafer. For ease of discussion, the overlay mark 130 will be described in context of measuring overlay between different layers of a substrate. It should be noted, however, that the overlay mark in this figure may also

be used to measure two or more separately generated patterns on a single layer of a substrate.

The overlay mark 130 includes a plurality of working zones 132 for determining the registration error between two wafer layers in two different directions. In the illustrated embodiment, the overlay mark 130 includes eight rectangularly shaped working zones 132, which are configured to substantially fill its perimeter 71. The working zones 132 represent the actual areas of the mark that are used to calculate alignment between different layers of the wafer. As mentioned previously, the working zones 132 are spatially separated from one another so that they do not overlap portions of an adjacent working zone. In this particular configuration, some of the working zones are separated via exclusion zones while other working zones are positioned next to an adjacent working zone. For example, working zone 132B is separated from working zones 132 E and F via an exclusion zone 133 while working zones 132E and F are positioned next to one another at their edges.

To facilitate discussion, the working zones 132 are grouped into a first working group 134 and a second working group 136. The first working group 134 includes four working zones 132A-D that are configured to provide overlay information in a first direction. By way of example, the first direction may be the Y direction. Of the four working zones 132A-D, two of them 132A and D are disposed in the first layer and two of them 132 B and C are disposed in the second layer (the first layer is represented by cross hatching, the second layer is represented by no cross hatching). As should be appreciated, for this mark configuration and in the case of zero overlay error (as shown), the centers of symmetry 135 of working zones 132A&D and working zones 132B&C coincide exactly. The second working group 136 includes four working zones 132E-H configured to provide overlay information in a second direction that is perpendicular to the first direction. By way of example, the second direction may be the X direction. Of the four working zones 132E-H, two of them 132 E and H are disposed in the first layer and two of them 132 F and G are disposed in the second layer (the first layer is represented by cross hatching, the second layer is represented by no cross hatching). Similarly to the above, for this mark configuration and in the case of zero overlay (as shown), the centers of

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symmetry 137 of working zones 132E&H and working zones 132F&G coincide exactly.

As should be appreciated, each of the groups 134 and 136 represents an “X” –  
5 configured mark (albeit offset). For example, working group 134 includes working zones 132A&D, which are on the same first layer and in diagonally opposed positions relative to one another, and working zones 132B&C, which are on the same second layer and in diagonally opposed positions relative to one another. Further, working zones 132A&D are angled relative to working zones 132B&C. Further still, working  
10 zone 132A is spatially offset from working zone 132D, and working zone 132B is spatially offset from working zone 132D.

In addition, working group 136 includes working zones 132E&H, which are on the same first layer and in diagonally opposed positions relative to one another, and working zones 132F&G, which are on the same second layer and in diagonally opposed positions relative to one another. Further, working zones 132E&H are angled relative to working zones 132F&G. Further still, working zone 132E is spatially offset from working zone 132H, and working zone 132F is spatially offset from working zone 132G. In essence, this particular mark produces two “X” configured marks that are positioned orthogonal to each other, i.e., working group 194 and working group 196.  
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To elaborate further, a working zone on one layer is generally juxtaposed relative to a working zone on another layer. For example, in the first working group, working zone 132A is juxtaposed relative to working zone 132B and working zone 132C is juxtaposed relative to working zone 132D. Similarly, in the second working group, working zone 132E is juxtaposed relative to working zone 132H and working zone 132F is juxtaposed relative to working zone 132G. Of the two juxtaposed pairs, the working zone on the second layer is typically positioned closer to the center of the FOV than the working zone on the first layer. For example, working zones 132B and C and working zones 132 F and G are positioned closer to the center 142 of the FOV 144 than their juxtaposed working zones 132A and D and working zones 132 E and H, respectively. Furthermore, within each of the working groups, the juxtaposed pairs are positioned in an opposed relationship (e.g., diagonal) relative to the other  
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juxtaposed pair in the group. For example, juxtaposed pairs 132A&B are positioned opposite juxtaposed pairs 132C&D, and juxtaposed pairs 132E&F are positioned opposite juxtaposed pairs 132G&H.

5 As should be appreciated, in this particular mark, the configuration of the working zones is rotationally symmetric ( $\pm 90, 180, 270, 360$  degrees around the center of the mark). This is typically done to reduce the impact of radial and axial variations across the field of view of the metrology tool, as for example, radial and axial variations caused by non-uniform optical aberrations and illumination that may  
10 cause tool induced shifts (TIS). Radial variations generally refer to variations that radiate from the center of the mark to the outer regions of the mark. Axial variations generally refer to variations that occur in directions along the axis of the mark, as for example, in the X direction from the left to the right portions of the mark, and in the Y direction from the lower to the upper portions of the mark.

15 Each of the working zones 132A-H includes a periodic structure 138 comprised by a plurality of coarsely segmented lines 140. The linewidths, D, and spacings, s, of the coarsely segmented lines may be widely varied. As shown, each of the periodic structures 138 substantially fills the perimeter of its corresponding  
20 working zone 132. As should be appreciated, the periodic structures 138 are also disposed on the layer of its corresponding work zone 132.

For ease of discussion, the periodic structures 138 may be broken up into a first periodic structure 138A that is associated with the first working group 134 and a  
25 second periodic structure 138B that is associated with the second working group. As shown, the first periodic structures 138A are all oriented in the same direction, i.e., the coarsely segmented lines 140 are parallel and horizontally positioned relative to each other. The second periodic structures 138B are also all oriented in the same direction (albeit differently than the first periodic structures), i.e., the coarsely  
30 segmented lines 140 are parallel and vertically positioned relative to each other. As such, the periodic structures 138A in the first working group 134 are orthogonal to the periodic structures 138B in the second working group 136.

In one embodiment, the coarsely segmented lines of juxtaposed periodic structures are aligned with one another, i.e., if we ignore the different layers they appear to be continuous gratings. For example, the coarsely segmented lines of working zone 132A may align with the coarsely segmented lines of working zone 132B and coarsely segmented lines of working zone 132C may align with the coarsely segmented lines of working zone 132D. In addition, the coarsely segmented lines of working zone 132E may align with the coarsely segmented lines of working zone 132F and coarsely segmented lines of working zone 132G may align with the coarsely segmented lines of working zone 132H.

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Fig. 10 is a top plan view of the overlay mark 150, in accordance with an alternate embodiment of the invention. In this particular embodiment, the coarsely segmented lines 140 are formed by a plurality of finely segmented elements 152. The finely segmented elements 152 generally correspond to the finely segmented elements 178 described in Fig. 2.

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Fig. 11 is a top plan view of an overlay mark 170, in accordance with an alternate embodiment of the present invention. By way of example, the overlay mark 170 may generally correspond to the overlay mark shown in Figs. 6 & 9. Similarly to the overlay mark 130, overlay mark 170 contains eight working zones 172A-H for determining the registration error between two wafer layers in two different directions (one layer is represented by cross-hatching, the other is not). Each of the working zones includes a periodic structure 174 comprised by a plurality of coarsely segmented lines 176. Similarly to the overlay mark 90, each of the working zones 172, are configured to accommodate an additional structure 178 in the center of the mark 170. In the illustrated embodiment, the working zones 172A-H are disposed around the outer region of the mark while the additional structure 178 is disposed in the center of the mark. The additional structure 178 may represent a standard box in box structure as described in the background of this application. As such, the mark 170 can be acquired and measured by standard box in box metrology tools and algorithms, i.e., existing equipment and software may be used.

Fig. 12 is a top plan view of an overlay mark 190, in accordance with an alternate embodiment of the present invention. By way of example, overlay mark 190

may correspond to the mark of Fig. 9. Like the mark of Fig. 9, the overlay mark 190 of Fig. 12 is configured to measure overlay in two separate directions. As such, mark 190 obviates the need to have one mark for each direction in which overlay needs to be measured. In contrast to the mark of Fig. 9, the mark 190 includes triangularly shaped working zones. Overlay mark 190 is shown in a configuration that results when the tested layers of a wafer are in perfect alignment.

The overlay mark 190 includes a plurality of working zones 192 for determining the registration error between two wafer layers in two different directions. In the illustrated embodiment, the overlay mark 140 includes eight triangularly shaped working zones 192, which are configured to substantially fill its perimeter . The working zones 192 represent the actual areas of the mark that are used to calculate alignment between different layers of the wafer. As mentioned previously, the working zones 192 are spatially separated from one another so that they do not overlap portions of an adjacent working zone. In this particular configuration, all of the working zones 192 are separated via exclusion zones 193.

To facilitate discussion, the working zones 192 are grouped into a first working group 194 and a second working group 196. The first working group 194 includes four working zones 192A-D that are configured to provide overlay information in a first direction. By way of example, the first direction may be the Y direction. Of the four working zones 192A-D, two of them 192A and D are disposed in the first layer and two of them 192 B and C are disposed in the second layer (the first layer is represented by solid lines, the second layer is represented by dashed lines). As should be appreciated, for this mark configuration and in the case of zero overlay (as shown), the centers of symmetry 195 of working zones 192A&D and working zones 192B&C coincide exactly. The second working group 196 includes four working zones 192E-H configured to provide overlay information in a second direction that is perpendicular to the first direction. By way of example, the second direction may be the X direction. Of the four working zones 192E-H, two of them 192 E and H are disposed in the first layer and two of them 192 F and G are disposed in the second layer (the first layer is represented by solid lines, the second layer is represented by dashed lines). Similarly to the above, for this mark configuration and in the case of zero overlay (as shown), the centers of symmetry 197 of working zones

192E&H and working zones 192F&G coincide exactly. In addition, and all of the working zones 192 are equally positioned relative to the center of the mark.

As should be appreciated, each of the groups 194 and 196 represents an “X” – configured mark. For example, working group 194 includes working zones 192A&D, which are on the same first layer and in diagonally opposed positions relative to one another, and working zones 192B&C, which are on the same second layer and in diagonally opposed positions relative to one another. Further, working zones 192A&D are angled relative to working zones 192B&C. Further still, working zone 192A is spatially offset from working zone 192D, and working zone 192B is spatially offset from working zone 192D.

In addition, working group 136 includes working zones 192E&H, which are on the same first layer and in opposed positions relative to one another, and working zones 192F&G, which are on the same second layer and in opposed positions relative to one another. Further, working zones 192E&H are angled relative to working zones 192F&G. Further still, working zone 192E is spatially offset from working zone 192H, and working zone 192F is spatially offset from working zone 192G. In essence, this particular mark produces two “X” configured marks that are positioned orthogonal to each other, i.e., working group 194 and working group 196.

To elaborate further, a working zone on one layer is generally juxtaposed relative to a working zone on another layer. For example, in the first working group, working zone 192A is juxtaposed relative to working zone 192B and working zone 192C is juxtaposed relative to working zone 192D. Similarly, in the second working group, working zone 192E is juxtaposed relative to working zone 192H and working zone 192F is juxtaposed relative to working zone 192G. For this mark configuration and in the case of zero overlay (as shown), all of the working zones 192 are equally positioned relative to the center of the mark. Furthermore, within each of the working groups, the juxtaposed pairs are positioned in an opposed relationship (e.g., upper/lower and right/left) relative to the other juxtaposed pair in the group. For example, juxtaposed pairs 192A&B are positioned opposite juxtaposed pairs 192C&D, and juxtaposed pairs 192E&F are positioned opposite juxtaposed pairs 192G&H.

As should be appreciated, in this particular mark, the configuration of the working zones is rotationally symmetric ( $\pm 90, 180, 270, 360$  degrees around the center of the mark) without biasing the center or periphery with one or other layer, 5 i.e., the mark is invariant. This is typically done to reduce the impact of radial and axial variations across the field of view of the metrology tool, as for example, radial and axial variations caused by optical aberrations and illuminations that may cause tool induced shifts (TIS).

10 Each of the working zones 192 includes a periodic structure 198 comprised by a plurality of coarsely segmented lines 200. The linewidths, D, and spacings, s, of the coarsely segmented lines may be widely varied. As shown, each of the periodic structures 198 substantially fills the perimeter of its corresponding working zone 192. As should be appreciated, the periodic structures 198 are also disposed on the layer of 15 its corresponding work zone 192.

For ease of discussion, the periodic structures 198 may be broken up into a first periodic structure 198A that is associated with the first working group 194 and a second periodic structure 198B that is associated with the second working group 196. 20 As shown, the first periodic structures 198A are all oriented in the same direction, i.e., the coarsely segmented lines 190A are parallel and horizontally positioned relative to each other. The second periodic structures 198B are also all oriented in the same direction (albeit differently than the first periodic structures), i.e., the coarsely segmented lines 198B are parallel and vertically positioned relative to each other. As 25 such, the periodic structures 198A in the first working group 194 are orthogonal to the periodic structures 198B in the second working group 196. Furthermore, in order to accommodate each zone within the FOV, the coarsely segmented lines 190 decrease in length as they move from the outer regions of the mark to the inner regions of the mark. Although not shown, the coarsely segmented line may be formed by a plurality 30 of finely segmented elements to further enhance this mark.

Fig. 13 is a top plan view of an overlay mark 210, in accordance with an alternate embodiment of the present invention. As shown, mark 210 has the same general layout and characteristics as mark 190 of Fig. 12, i.e., eight triangularly

shaped working zones. Mark 210 differs from mark 190, however, in that it biases the center of the mark with a grating pattern 212 formed on one of the two layers. The grating pattern 212 is typically used in cases where the mark quality in one layer is poorer than the mark quality in the other layer due to contrast or graininess. That is,  
5 the information (e.g., edges) in a layer where contrast is low is increased.

Alternatively, biasing the center of the FOV with one layer may protect it from process damage. The grating pattern 212 may be widely varied. For example, grating pattern may include any number of lines in any number of distributions and sizes. In this particular embodiment, the grating pattern is formed on the second layer  
10 and it consists of groups of two coarsely segmented lines 214 that alternate in direction (e.g., X and Y directions) around the center of the mark.

Fig. 14 is a top plan view of an overlay mark 220, in accordance with an alternate embodiment of the present invention. By way of example, overlay mark 220 may generally correspond to the overlay mark shown in Fig. 9. Like the overlay mark of Fig. 9, overlay mark 220 is configured to measure overlay in two separate directions. As such, mark 220 obviates the need to have one mark for each direction in which overlay needs to be measured. Overlay mark 220 is shown in a configuration that results when the tested layers of a wafer are in perfect alignment.  
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The overlay mark 220 includes a plurality of working zones 222 for determining the registration error between two wafer layers in two different directions. In the illustrated embodiment, the overlay mark 222 includes sixteen square shaped working zones 222, which are configured to substantially fill its perimeter. Each of the working zones 222 includes a periodic structure of coarsely segmented lines. Although not shown, it should be appreciated that in some embodiments the coarsely segmented lines may be formed by a plurality of finely segmented elements, as for example, any configuration described or shown herein (i.e., Figs. 3-5).  
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Of the 16 working zones, 8 of the working zones 222A are oriented in the X direction and 8 of the working zones 222B are oriented in the Y direction (as shown by the periodic structures disposed therein). Of the 8 working zones 222, in any given orientation (A or B), 4 of the working zones 222' are printed in a first layer

(represented by cross hatching) while 4 of the working zones 222” are printed in a second layer (not represented by cross hatching). The orientation of the working zones may be described in a variety of ways. For example, the working zones 222 may be distributed into four groups 224A-D that form the four corners of the square shaped mark 220. Each of these groups 224A-D is equally represented by working zones formed on different layers and in different direction. That is, each group includes four different working zones, as for example, working zones 222A’, 222A”, 222B’ and 222B”.

The working zones 222 may also be distributed into four groups 224E-H, each of which represents an “X” – configured mark (albeit offset). In this case, the “X”-configured mark is formed by the corners of a 3 by 3 working zone group. Of the four groups, two of them determine overlay in the X-direction and two of them determine overlay in the Y direction. For example, working group 224E and F, which include diagonally opposed and spatially offset working zones 222B’ & 222B”, determine overlay in the Y-direction. Furthermore, working group 224G and H, which include diagonally opposed and spatially offset working zones 222A’ & 222A”, determine overlay in the X-direction.

Fig. 15 is a top plan view of an overlay mark 240, in accordance with an alternate embodiment of the present invention. By way of example, overlay mark 240 may generally correspond to the overlay mark shown in Fig. 14. Like the overlay mark of Fig. 13, overlay mark 240 is configured to measure overlay in two separate directions. As such, mark 240 obviates the need to have one mark for each direction in which overlay needs to be measured. Overlay mark 240 is shown in a configuration that results when the tested layers of a wafer are in perfect alignment.

The overlay mark 240 includes a plurality of working zones 242 for determining the registration error between two wafer layers in two different directions. In the illustrated embodiment, the overlay mark 240 includes sixteen square shaped working zones 242, which are configured to substantially fill its perimeter. Each of the working zones 242 includes a periodic structure of coarsely segmented lines. Although not shown, it should be appreciated that in some

embodiments the coarsely segmented lines may be formed by a plurality of finely segmented elements.

Of the 16 working zones, 8 of the working zones 242A are oriented in the X direction and 8 of the working zones 242B are oriented in the Y direction (as shown by the periodic structures disposed therein). Of the 8 working zones 242, in any given orientation (A or B), 4 of the working zones 242' are printed in a first layer (represented by cross hatching) while 4 of the working zones 242'' are printed in a second layer (not represented by cross hatching). The orientation of the working zones may be described in a variety of ways. For example, the working zones 242 may be distributed into four groups 244A-D that form the four corners of the square shaped mark 240. The groups that are opposed at vertical angles are identical, i.e., the working zones therein are oriented the same way. Of the four working zones in each group 244A-D, two of them represent the same layer and direction, and two of them represent a different same layer and direction. The working zones that are opposed at vertical angles to one another in these groups are identical, i.e., they represent the same layer and direction. For example, groups 244A & D include opposed working zones 242A' and opposed working zones 242B'', and groups 244B & C include opposed working zones 242B' and opposed working zones 242A''.

The working zones 242 may also be distributed into four groups 244E-H, each of which represents an "X" – configured mark (albeit offset). In this case, the "X"-configured mark is formed by the corners of a 3 by 3 working zone group. Of the four groups, two of them determine overlay in the X-direction and two of them determine overlay in the Y direction. For example, working group 244E and F, which include opposing working zones 242B'& 242B'', determine overlay in the Y-direction. Furthermore, working group 244G and H, which include opposing working zones 242A'& 242A'', determine overlay in the X-direction.

Fig. 16 is a top plan view of an overlay mark 250, in accordance with an alternate embodiment of the present invention. By way of example, overlay mark 250 may generally correspond to the overlay mark shown in Fig. 13. Like the overlay mark of Fig. 14, overlay mark 250 is configured to measure overlay in two separate directions. As such, mark 250 obviates the need to have one mark for each direction

in which overlay needs to be measured. Overlay mark 250 is shown in a configuration that results when the tested layers of a wafer are in perfect alignment.

The overlay mark 250 includes a plurality of working zones 252 for  
5 determining the registration error between two wafer layers in two different directions. In the illustrated embodiment, the overlay mark 250 includes sixteen square shaped working zones 252, which are configured to substantially fill its perimeter. Each of the working zones 252 includes a periodic structure of coarsely segmented lines. Although not shown, it should be appreciated that in some  
10 embodiments the coarsely segmented lines may be formed by finely segmented elements.

Of the 16 working zones, 8 of the working zones 252A are oriented in the X direction and 8 of the working zones 252B are oriented in the Y direction (as shown  
15 by the periodic structures disposed therein). Of the 8 working zones 252, in any given orientation (A or B), 4 of the working zones 252' are printed in a first layer (represented by cross hatching) while 4 of the working zones 252" are printed in a second layer (not represented by cross hatching). As shown, the working zones 252 may be distributed into four groups 254E-H, each of which represents an "X" –  
20 configured mark (albeit offset). In this case, the "X"-configured mark is formed by the corners of a 3 by 3 working zone group. Of the four groups, two of them determine overlay in the X-direction and two of them determine overlay in the Y direction. For example, working group 254E and F, which include opposing working zones 252B' & 252B", determine overlay in the Y-direction. Furthermore, working  
25 group 254G and H, which include opposing working zones 252A' & 252A", determine overlay in the X-direction.

Fig. 17 is a top plan view of an overlay mark 270, in accordance with an alternate embodiment of the present invention. By way of example, overlay mark 270  
30 may generally correspond to the overlay mark shown in Fig. 13. Like the overlay mark of Fig. 14, overlay mark 270 is configured to measure overlay in two separate directions. As such, mark 270 obviates the need to have one mark for each direction in which overlay needs to be measured. Overlay mark 270 is shown in a configuration that results when the tested layers of a wafer are in perfect alignment.

The overlay mark 270 includes a plurality of working zones 272 for determining the registration error between two wafer layers in two different directions. In the illustrated embodiment, the overlay mark 270 includes sixteen square shaped working zones 272, which are configured to substantially fill its perimeter. Each of the working zones 272 includes a periodic structure of coarsely segmented lines. Although not shown, it should be appreciated that in some embodiments the coarsely segmented lines may be formed by finely segmented elements.

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Of the 16 working zones, 8 of the working zones 272A are oriented in the X direction and 8 of the working zones 272B are oriented in the Y direction (as shown by the periodic structures disposed therein). Of the 8 working zones 272, in any given orientation (A or B), 4 of the working zones 272' are printed in a first layer (represented by cross hatching) while 4 of the working zones 272'' are printed in a second layer (not represented by cross hatching). Furthermore, of the 8 working zones 272, in any given orientation (A or B), 4 of the working zones 272 have a periodic structure M with a first period (represented by thinner lines) while 4 of the working zones 272 have a periodic structure N with a second period that is different than the first period (represented by wider lines).

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The orientation of the working zones may be described in a variety of ways. For example, the working zones 272 may be distributed into four groups 274A-D that form the four corners of the square shaped mark 270. The groups that are opposed at vertical angles are identical, i.e., the working zones therein are oriented the same way. Each of these groups 274A-D includes four different working zones, which may represent different combinations of layers, directions or period. The working zones that are opposed at vertical angles to one another in these groups represent the same direction, but different layers and periods. For example, groups 272A & D include working zone 272A-M' that is opposed to working zone 272B-N' and working zone 272A-M'' that is opposed to working zone 272B-N'', and groups 272B & C include working zone 272B-M' that is opposed to working zone 272A-N' and working zone 272B-M'' is opposed to working zone 272A-N''.

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The working zones 272 may also be distributed into four groups 274E-H, each of which represents an “X” – configured mark (albeit offset). In this case, the “X”-configured mark is formed by the corners of a 3 by 3 working zone group. Of the four groups, two of them determine overlay in the X-direction and two of them determine overlay in the Y direction. Furthermore, two of them represent a first period and two of them represent a second period. For example, working group 274E, which includes opposing working zones 272B-M’ & 272B-M”, has a first period that determines overlay in the Y-direction, and working group 274F, which includes opposing working zones 272B-N’ & 272B-N”, has a second period that determines overlay in the Y-direction. Furthermore, working group 274G, which include opposing working zones 272A-M’ & 272A-M”, has a first period that determines overlay in the X-direction, and working group 274H, which include opposing working zones 272A-N’ & 272A-N”, has a second period that determines overlay in the X-direction.

As should be appreciated, this configuration can result in improved process robustness and/or improved contrast for one of the two grating periods, allowing selection of the optimized period and or line width for a specific process.

Fig. 18 is a top plan view of an overlay mark 290, in accordance with an alternate embodiment of the present invention. By way of example, overlay mark 290 may generally correspond to the overlay mark shown in Fig. 13. Like the overlay mark of Fig. 14, overlay mark 290 is configured to measure overlay in two separate directions. As such, mark 290 obviates the need to have one mark for each direction in which overlay needs to be measured. Unlike the overlay mark of Fig. 13, overlay mark 290 is also configured to determine the relative shift between three successive layers of a wafer or between three separately generated patterns on a single layer of a wafer. For ease of discussion, the overlay mark 290 will be described in context of measuring overlay between different layers of a substrate. It should be noted, however, that the overlay mark in this figure may also be used to measure two or more separately generated patterns on a single layer of a substrate. Overlay mark 290 is shown in a configuration that results when the tested layers of a wafer are in perfect alignment.

The overlay mark 290 includes a plurality of working zones 292 for determining the registration error between three wafer layers in two different directions. In the illustrated embodiment, the overlay mark 290 includes sixteen square shaped working zones 292, which are configured to substantially fill its perimeter<sup>7</sup>. Each of the 5 working zones 292 includes a periodic structure of coarsely segmented lines. Although not shown, it should be appreciated that in some embodiments the coarsely segmented lines may be formed by finely segmented elements.

Of the 16 working zones 292, 8 of the working zones 292A are oriented in the X 10 direction and 8 of the working zones 292B are oriented in the Y direction (as shown by the periodic structures disposed therein). Furthermore, 8 of the working zones 292' are printed in a first layer (represented by cross hatching), 4 of the working zones 292'' are printed in a second layer (not represented by cross hatching), and 4 of the 15 working zones 292''' are printed in a third layer (represented by fill). In this particular embodiment, the first layer (also represented by a single prime) is disposed over the second layer (also represented by a double prime) and the second layer is disposed over the third layer (also represented by a triple prime). By way of example, the first layer may represent a resist layer, the second layer may represent a first metal layer, and the third layer may represent a second metal layer.

It should be noted that the above configuration may be widely varied. For 20 example, of the 8 working zones in any given orientation (A or B), 2 may be printed in a first layer, while each additional pair of gratings may be printed in up to any of 3 previous layers.

All of the overlay marks described above are configured to at least balance non-uniformities caused by the metrology tool (e.g., aberrations and illumination) and/or by the process (e.g., dishing and erosion). For example, some of the marks may be 25 configured to reduce the impact of radial variations while others may be configured to reduce the impact of axial variations on the overlay measurements.

Fig. 19 is a simplified diagram of an overlay measurement system or 30 metrology tool 320 that may be used to measure overlay in any of the marks described above via imaging. Imaging is a very developed technology with large user

acceptance, and components that are readily available to the user. As is generally well known, imaging is an effective way to collect a large amount of information at any one time. That is, all points within the mark may be measured simultaneously.

Furthermore, imaging allows a user to see what is actually being measured on the wafer. The dimensions of various components are exaggerated to better illustrate this embodiment. The overlay measurement system 320 is arranged to determine overlay error via one or more overlay targets 322 disposed on a wafer 324. In most cases, the overlay targets 322 are positioned within the scribe lines of the wafer 324. As is generally well known, scribe lines are the areas of the wafer used for sawing and dicing the wafer into a plurality of dies. It should be noted, however, that this is not a limitation and that the position of the targets may vary according to the specific needs of each device design. As shown, the overlay measurement system 320 includes an optical assembly 326 and a computer 328. The optical assembly 326 is generally arranged to capture the images of the overlay target 322. The computer, on the other hand, is generally arranged to calculate the relative displacement of the elements of the overlay target from the captured images.

In the illustrated embodiment, the optical assembly 326 includes a light source 330 (e.g., incoherent or coherent, although incoherent is generally preferred) arranged to emit light 332 along a first path 334. The light 332 is made incident on a first lens 335, which focuses the light 332 onto a fiber optic line 336 configured to pass the light 332 therethrough. When the light 332 emerges from fiber optic line 336, it then passes through a second lens 338, which is arranged to collimate the light 332. The collimated light 332 then continues on its path until it reaches a beam splitter cube 340, which is arranged to direct the collimated light onto a path 342. The collimated light 332 continuing along path 342 is made incident on an objective lens 344, which focuses the light 332 onto the wafer 324.

The light 332, which reflects off of the wafer 324, is then collected by the objective lens 344. As should be appreciated, the reflected light 332 that is collected by the objective lens 344 generally contains an image of a portion of the wafer 324, as for example, the image of the overlay target 322. When the light 332 leaves the objective 344, it continues along path 342 (backwards) until it reaches the beam splitter cube 340. In general, the objective lens 344 manipulates the collected light in

a manner that is optically reverse in relation to how the incident light was manipulated. That is, the objective lens 344 re-collimates the light 332 and directs the light 332 towards the beam splitter cube 340. The beam splitter cube 340 is arranged to direct the light 332 onto a path 346. The light 332 continuing on path 346 is then 5 collected by a tube lens 350, which focuses the light 332 onto a camera 352 that records the image of the wafer 324, and more particularly the image of the target 322. By way of example, the camera 352 may be a charge couple device (CCD), a two-dimensional CCD, or linear CCD array. In most cases, the camera 352 transforms the recorded image into electrical signals, which can be used by, and which are sent to the 10 computer 328. After receiving the electrical signals, the computer 328 performs analysis algorithms that calculate the overlay error of the image. Analysis algorithms will be described in greater detail below.

The system 320 further includes a frame grabber 354 that works with the 15 computer 328 and the camera 352 to grab images from the wafer 324. Although the frame grabber 354 is shown as a separate component, it should be noted that the frame grabber 354 may be part of the computer 328 and/or part of the camera 352. The frame grabber 354 typically has two functions - target acquisition and image grab. During target acquisition, the frame grabber 354 and computer 328 cooperate with a 20 wafer stage 356 to place the target in focus and to position the target as close as possible to the center of the field of view (FOV) of the metrology tool. In most cases, the frame grabber grabs a plurality of images (e.g., not the images used to measure overlay) and the stage moves the wafer between these grabs until the target is correctly positioned in the X, Y and Z directions. As should be appreciated, the X&Y 25 directions generally correspond to the field of view (FOV) while the Z direction generally corresponds to the focus. Once the frame grabber determines the correct position of the target, the second of these two functions is implemented (e.g., image grab). During image grab, the frame grabber 354 makes a final grab or grabs so as to capture and store the correctly positioned target images, i.e., the images that are used 30 to determine overlay.

After grabbing the images, information must be extracted from the grabbed images to determine the registration error. The extracted information may be digital information or in waveforms. Various algorithms may then be used to determine the

registration error between various layers of a semiconductor wafer. For example, a frequency domain based approach, a space domain based approach, Fourier transform algorithms, zero-crossing detection, correlation and cross-correlation algorithms and others may be used.

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Algorithms proposed for determining overlay via the marks described herein (e.g., marks that contain periodic structures) can generally be divided into a few groups. For instance, one group may relate to phase retrieval based analysis. Phase retrieval based analysis, which is often referred to as frequency domain based approaches, typically involves creating one dimensional signals by collapsing each of the working zones by summing pixels along the lines of the periodic structure. Examples of phase retrieval algorithms that may be used are described in U.S. Patent No. 6,023,338 issued to Bareket, U.S. Patent Application No. 09/603,120 (Attorney Docket No. KLA1P026) filed on June 22, 2000, and U.S. Patent Application No. 09/654,318 (Attorney Docket No. KLA1P029) filed on September 1, 2000, all of which are incorporated herein by reference.

Yet another phase retrieval algorithm that may be used is described in U.S. Application No. 09/697,025 filed on October 26, 2000, which is also incorporated herein by reference. The phase retrieval algorithm disclosed therein decomposes signals into a set of harmonics of the basic signal frequency. Quantitative comparison of different harmonics' amplitudes and phases provide important information concerning signals' symmetry and spectral content. In particular, the phase difference between the 1st and 2nd or higher harmonics of the same signal (calibrated with their amplitudes) measures the degree of the signal asymmetry. The major contributions to such asymmetry come from the optical misalignment and illumination asymmetry in the metrology tool (tool induced shifts), as well as process induced structural features (wafer induced shifts). Comparing this misregistration between the phases of the 1st and the 2nd harmonics for the signals acquired from different parts of the field of view on the same process layer may provide independent information about optical aberrations of the metrology tool. Finally, comparing these misregistrations from measurements at a given orientation with those obtained after rotating the wafer 180 degrees allows separation of the tool induced and wafer induced shifts due to asymmetry.

Yet another phase retrieval algorithm that may be used is Wavelet analysis. Wavelet analysis is somewhat similar to that described in the section above, however, now a dynamic window is moved across the one dimensional signal and the phase estimation is carried out in a more localized way. This is particularly of interest with use in the case of a chirped periodic structure.

Another group may relate to intensity correlation based methods. In this approach the centers of symmetry for each process layer is found separately by calculating the cross covariance of one signal with the reversed signal from the opposite part of the mark, from the same process layer. This technique is similar to techniques used today with regards to box in box targets.

The above techniques are brought by way of example and have been tested and demonstrated good performance. Other alternative algorithmic methods for calculation of overlay include other variations of auto & cross correlation techniques, error correlation techniques, error minimization techniques, such as minimization of absolute difference, minimization of the square of the difference, threshold based techniques including zero cross detection, and peak detection. There are also dynamic programming algorithms which can be used for searching for the optimal matching between two one-dimensional patterns. As mentioned above, the analysis algorithms and approaches may be utilized with respect to all of the various overlay marks described in the previous section.

Importantly, it should be noted that the above diagram and description thereof is not a limitation and that the overlay image system may be embodied in many other forms. For example, it is contemplated that the overlay measurement tool may be any of a number of suitable and known imaging or metrology tools arranged for resolving the critical aspects of overlay marks formed on the surface of the wafer. By way of example, overlay measurement tool may be adapted for bright field imaging microscopy, darkfield imaging microscopy, full sky imaging microscopy, phase contrast microscopy, polarization contrast microscopy, and coherence probe microscopy. It is also contemplated that single and multiple image methods may be used in order to capture images of the target. These methods include, for example,

single grab, double grab, single grab coherence probe microscopy (CPM) and double grab CPM methods. These types of systems, among others, are readily available commercially. By way of example, single and multiple image methods may be readily available from KLA-Tencor of San Jose, California.

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Fig. 20A is a simplified flow diagram illustrating a method 360 of calculating overlay, in accordance with one embodiment of the present invention. For ease of discussion, this method will be described via the mark shown in Fig. 9. This mark is now shown next to the flow diagram in Fig. 20B. The method 360 begins at step 362 where the working zones are selected from the captured image. By way example, for calculating X-overlay, working zones 132 E-H may be selected, and for calculating Y-overlay, working zones 132 A-D may be selected. After selecting the working zones, the process flow proceeds to step 364 where representative signals are formed for each of the selected working zones. This may be accomplished by collapsing the 2D images into 1D signals by averaging over X for Y-overlay calculations and by averaging over Y for X-overlay calculations. By way of example, Fig. 20C illustrates a first collapsed 1D signal for working zone 132A and a second collapsed 1D signal for working zone 132B. It should be noted, that Fig. 20C is representative of any of the pairs of juxtaposed working zones. After forming the signals, the process flow proceeds to step 366 where the overlay is determined by comparing the signals.

In one embodiment, this is accomplished via a covariance-based overlay algorithm, which is based upon calculation of the cross-correlation between the patterns belonging to the same process layers. As a result, the centers of symmetry for both layers are found, and their misregistration is essentially the overlay. The flowchart of this algorithm is shown in Figure 21.

In another embodiment, this is accomplished via a Fourier Decomposition overlay algorithm, which utilizes the periodical character of the grating structures. This algorithm decomposes signals acquired from the target patterns to a series of Fourier harmonics. Comparison of phases between the same order harmonics from different process layers calibrated to nominal pitch of the grating patterns serves then as a basis for overlay calculation. Accordingly, this algorithm provides several independent

overlay results - one for each Fourier order. The flowchart of this algorithm is shown in Figure 22.

Fig. 21 is a flow diagram illustrating a method 370 of calculating overlay  
5 using Covariance, in accordance with one embodiment of the present invention. By way of example method 370 may generally correspond to step 366 of Fig. 20A. The method 370 begins at step 372 where the signal cross-correlation is calculated. This is typically done with respect to opposing working zones. With regards to the mark of Fig. 9., the signal cross-correlation is calculated for working zone pairs 132A vs.  
10 reversed 132D, 132B vs. reversed 132C, 132E vs. reversed 132H, and 132F vs. reversed 132G. After calculating the signal cross correlation, the process flow proceeds to step 374 where the positions of cross-correlation maxima (sub pixel) are found. This is typically done for both of the layers, i.e., layer 1, which is represented by cross hatching, and layer 2, which is represented with no cross hatching. After finding the positions of cross correlation maxima, the process flow proceeds to step 15 376 where the overlay is determined by calculating the difference between the positions of cross correlation maxima. For example, the difference between the cross correlation maxima of working zones 132E&H (layer 1) – working zones 132 F&G (layer 2) determines the misregistration in the X-direction. In addition, the difference between the cross correlation maxima of working zones 132A&D (layer 1) – working zones 20 132 B&C (layer 2) determines the misregistration in the Y-direction.

Fig. 22 is a flow diagram illustrating a method 380 of calculating overlay using Fourier Decomposition, in accordance with one embodiment of the present invention. By way of example method 380 may generally correspond to step 366 of Fig. 20A. The method 380 begins at step 382 where the signals are fitted to a Fourier series and their phases are extracted. After fitting and extracting, the process flow proceeds to step 384 where the phase difference between juxtaposed working zones is found. For example, in the Y direction, the phase difference is found between working zones 132A and 132B, as well as, between working zones 132C and 132D. In addition, in the X direction, the phase difference is found between working zones 30 132E and 132F, as well as, between working zones 132G and 132H. After finding the phase difference, the process flow proceeds to step 386 where the overlay is determined by calculating the difference between the phase differences of the

previous step for a given direction. For example, the average of the difference between the phase difference of working zones 132E&F and the phase difference of working zones 132 G&H determines the misregistration in the X-direction. In addition, the average of the difference between the phase difference of working zones 5 132A&B and the phase difference of working zones 132 C&D determines the misregistration in the Y-direction. In order to obtain data that corresponds to positions of the layers, the phase difference is multiplied by the pitch and divided by  $2\pi$ .

10 In summary, a method of designing overlay marks in accordance with the principles set forth above will now be described.

Fig. 23 is a flow diagram illustrating a method 390 of designing an overlay mark, in accordance with one embodiment of the present invention. By way of example, the overlay mark may generally correspond to any of the overlay marks described above. The overlay mark is generally provided to determine the relative shift between two or more successive layers of a substrate or between two or more separately generated patterns on a single layer of a substrate. In accordance with one embodiment of the present invention, the overlay mark comprises a plurality of 15 elements that form the pattern of the overlay mark. Each of these elements may be configured to contain information for measuring overlay in a more precise or accurate manner. By way of example, the elements may generally correspond to the working zones, periodic structures of coarsely segmented elements and finely segmented 20 elements.

25 The method 390 begins at step 392 where the geometry of a first element of the mark is optimized according to a first scale. This is typically accomplished by identifying the upper and lower limits of the first scale and fine tuning the geometry of the first geometry between the upper and lower limits. In one embodiment, the first 30 scale corresponds to the metrology kernel scale, which defines the boundaries of the regions that contain information about the two different layers or patterns between which overlay is to be measured. The metrology kernel scale has characteristic dimensions in the 10's of microns. For example, the metrology kernel scale may

range from about 4 microns to about 10 microns, and more particularly from about 5 microns to about 10 microns.

In most cases, the metrology kernel scale is based on metrology tool  
5 limitations, process issues and circuit design criteria. Metrology tool limitations  
generally refers to limitations associated with the metrology tool (both for a line of  
tools and a specific tool within the line). For example, metrology tool limitations may  
include the size and shape of the field of view of the metrology tool used to measure  
overlay, the minimum amount of target area needed to effectively measure overlay  
10 and asymmetrical aberration and illumination field distributions created by the  
components of the metrology tool. Process robustness issues generally refer to  
restrictions associated with the robustness of the process, as for example, etching,  
deposition, chemical-mechanical polishing (CMP) and the like. For example, there  
may be parts of the mark that are more susceptible to process damage or process  
15 variation and thus they should be avoided. Circuit design criteria generally refers to  
the rules used to design the overall circuit pattern. For example, the circuit design  
rules may include scribe line limitations that pertain to the overlay mark budget, i.e.,  
the mark is typically positioned inside the scribe line of the wafer. The scribe line is  
the place on the wafer where the wafer separated into a plurality of dies via sawing or  
20 dicing.

Once the first scale is identified, the geometry of the first element, which  
generally contains information relating to the layers or patterns between which  
overlay is measured, may be fine-tuned to find a mark layout that works best within  
25 this scale. In one embodiment, the first element corresponds to working zones, which  
define the different layers or patterns of the overlay mark, and which represent the  
actual area of the overlay mark that is used for overlay measurements. The term  
“geometry” generally refers to the size, shape, and/or distribution of the first element,  
i.e., the working zones. In one embodiment, fine-tuning is implemented by defining  
30 the perimeter of the mark (e.g., FOV) and dividing the mark into a plurality of  
working zones that are configured to minimize the impact of asymmetries on the  
measurement of overlay. By way of example, the working zones may be configured  
to minimize the impact of optical aberrations and illuminations on tool induced shifts  
in the resultant overlay measurement. In addition, the working zones may be

configured to minimize the impact of process variations on wafer induced shifts in the resultant overlay measurement.

After optimizing the geometry of the first element, the process flow proceeds  
5 to step 394 where the geometry of a second element of the mark is optimized  
according to a second scale. This is typically accomplished by identifying the upper  
and lower limits of the second scale and fine tuning the geometry of the second  
element between the upper and lower limits. In one embodiment, the second scale  
corresponds to the image resolution scale, which defines the boundaries between  
10 structures within a given process layer. The image resolution scale has characteristic  
dimensions in the 1 micron range. For example, the image resolution scale may range  
from about 0.3 microns to about 2 microns, and more particularly from about .5  
microns to about 1 micron.

15 In most cases, the image resolution scale is based on metrology tool  
limitations and process robustness issues. Metrology tool limitations generally refers  
to limitations associated with the metrology tool (both for a line of tools and a specific  
tool within the line). For example, metrology tool limitations may include the image  
resolution of the tool, i.e., the ability to capture an image, the algorithms used by the  
20 tool to calculate the overlay error and the aberration and illumination field  
distributions of the tool. Process robustness issues generally refer to restrictions that  
are created by the materials and processes that are used to form the layers and patterns  
on the wafer, as for example, etching, deposition, chemical-mechanical polishing  
(CMP) and the like. For example, for specific processes, such as aluminum coated,  
25 chemically mechanically polished tungsten, it is advantageous for the geometry of the  
second element to be one micron or less in order to diminish the impact of  
asymmetries resultant from the polishing and aluminum deposition processes. In  
other cases, where the metal grain size is large, it may be preferable that the lines be  
larger than 1 micron, as for example, up to two microns.

30 Once the second scale is identified, the geometry of the second element, which  
generally contains the actual spatial information regarding the relative positions of the  
mark components that is encoded and transferred to the metrology tool, may be fine-  
tuned to find a mark layout that works best within this scale. In one embodiment, the

second element corresponds to a periodic structure of coarsely segmented lines that is positioned within each of the working zones of the first element. The term “geometry” generally refers to the size, shape, and/or distribution of the second element i.e., the periodic structure of coarsely segmented lines (e.g., linewidths and spacings). In one embodiment, the periodic structures via the coarsely segmented lines are configured to enhance the measurement of overlay by balancing the image resolution of the tool with the process.

After optimizing the geometry of the second element, the process flow proceeds to step 396 where the geometry of a third element of the mark is optimized according to a third scale. This is typically accomplished by identifying the upper and lower limits of the third scale and fine tuning the geometry of the third element between the upper and lower limits. In one embodiment, the third scale corresponds to the lithography resolution scale, which defines the boundaries of sub-structures within a given structure. The lithography resolution scale has characteristic dimensions in the 0.1 micron range. For example, the lithography resolution scale may range from about 0.01 microns to about 0.5 microns, and more particularly from about .05 microns to about .18 microns.

In most cases, the lithography resolution scale is based on circuit design rules, process robustness issues and metrology tool limitations. Circuit design rules generally refer to the rules used to design the overall circuit pattern. For example, the circuit design rules may include the geometry of the circuit devices (e.g., feature size and density). Process robustness issues generally refer to restrictions that are created by the materials and processes that are used to form the layers and patterns on the wafer, as for example, etching, deposition, chemical-mechanical polishing (CMP) and the like. Metrology tool limitations generally refer to limitations associated with the metrology tool (both for a line of tools and a specific tool within the line). For example, metrology tool limitations may include contrast requirements of the tool, i.e., the ability to resolve the larger structures, which are comprised by the smaller sub-structures.

Once the third scale is identified, the geometry of the third element, which generally contains information reflective of the circuit structures themselves, may be

fine-tuned to find a mark layout that works best within this scale. In one embodiment, the third element corresponds to the finely segmented elements that form the plurality of coarsely segmented lines of the periodic structures of the second element. The term “geometry” generally refers to the size, shape, and/or distribution of the third element,  
5 i.e., the finely segmented elements. In one embodiment, the finely segmented elements are configured to minimize the adverse effects of the process so as to improve the accuracy and precision of the overlay measurements. In another embodiment, the structure is optimized so as to minimize the difference in stepper PPE between the target and the actual device, as described previously in Figs 3-5.

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While this invention has been described in terms of several preferred embodiments, there are alterations, permutations, and equivalents, which fall within the scope of this invention.

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For example, although the invention has been described in terms of manufacturing semiconductor devices, it should be realized that the invention may also be suitable for manufacturing other types of devices such as microfabrication of optical or optoelectronic devices, microfabrication of magnetic storage media or magnetic storage read/write or input/output devices, microfabrication using  
20 lithographic patterning in general, to include photolithography down to 100 nm exposure wavelengths, extreme-ultraviolet lithography with wavelengths 10nm to 100nm, Xrays lithography with wavelengths <10nm, electron beam lithography, ion beam patterning, or mixed lithography using more than one of these methods.

25

In addition, besides supplying data for overlay measurements, periodic structure targets are capable of providing much additional information for target, stepper and metrology tool diagnostics (e.g., contrast, sharpness, graininess, acquisition quality and symmetry metrics). For example, comparison of overlay, precision, TIS, and TIS variability results obtained by Covariance and Fourier  
30 Decomposition methods can serve as one such instrument. Measurement of phase difference between different Fourier harmonics from the same signal gives important information concerning symmetry of the marks due to process imperfectness, aberrations or illumination problems. Performing the same analysis for the target rotated by 180° allows the separation of asymmetries on the wafer from those due to

the metrology tool. Filling the whole FOV by target structures allows the selection of different working zones, thus providing information about variations within a single target and allowing additional optimization. Finally, grating targets provide an opportunity for simpler diagnostic of the target's tilt in FOV.

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Furthermore, although the algorithms have been described as utilizing one dimensional arrays of information, it should be noted that they may also be applied to two dimensional arrays of information.

10        Moreover, although the marks herein have been described for measuring overlay, they may also be used for one or more of the following measurements or applications: CD, exposure monitoring, resist profile monitoring, focus monitoring, and the like.

15        It should also be noted that there are many alternative ways of implementing the methods and apparatuses of the present invention. It is therefore intended that the following appended claims be interpreted as including all such alterations, permutations, and equivalents as fall within the true spirit and scope of the present invention.

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